

Wuhan Net-Light Technology Co., Ltd

WDP863-59C1

10GB/s 80Km SFP+ 1550nm Transceiver with DDM function

Features

- Compliant to SFP+ MSA
- Fully ROHS Compliant
- All metal housing for superior EMI performance
- Data rate from 8.5Gbps to 10Gbps
- Uncooled 1550nm EML DFB Laser
- APD receiver
- Up to 80km
- LC duplex connector
- Hot pluggable 20pin connector

Applications

• 10GFC

Standard

- SFF-8431 Rev 3.0
- SFF-8472 Rev 10.2
- 10GFC Rev 4.0
- FC-PI-4 Rev 7.0

- Low power consumption < 1.2W
- \bullet 0°C to 70°C operating wide temperature range
- Single +3.3V \pm 5% power supply
- Digital Diagnostic Monitoring sff-8472 Rev 10.2 compliant
- Real time monitoring of : Transmitted optical power Received optical power Laser bias current Temperature
 - Supply voltage

Ordering Information

	Specifications									
Part No	Packag e	Data rate	Tx	Optical Power	Rx	Sen	Temp	Reach	other	Application
WDP863-59C1	SFP+	10Gbps	1550nm DFB	$0\sim$ +4dBm	APD	<-23dBm	0∼70℃	80km	DDM	10GBASE-ZR/ 8G/10GFC



Descriptions

The WDP863-59C1 1550nm EML DFB 10Gigabit Transceiver is designed to transmitter and receive serial optical data over single mode optical fiber with 80km. They are compliant with SFF-8431, SFF-8432, 10GFC Rev4. 0, FC-PI-4 Rev7. 0. The transmitter convets serial CML electrical data into serial optical Data compliant with the IEEE802. 3ae standard. An open collector compatible Transmit Disable (Tx_Dis) is provided .When Tx_D Dis is asserted high, Transmitter is turned off. The receiver converts serial optical data into serial CML electrical data .An open collector compatible loss of signal is provided .The RX_LOS signal indicates insufficient optical power for reliable signal reception at the receiver. Digital diagnostics functions are available via 2-wire serial interface , as specified in sff-8472.

The optical output can be disabled by a TTL logic high-level input of Tx Disable, and the system also can disable the module via I2C. Tx Fault is provided to indicate that degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner. The system can also get the LOS (or Link)/Disable/Fault information via I2C register access.

Block Diagram

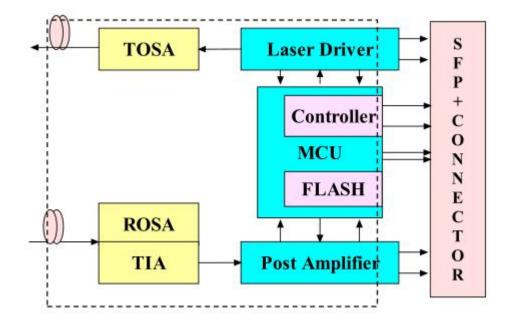


Figure 1. Transceiver functional diagram



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Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Storage Temperature	Ts	-40		85	° C	
Storage Ambient Humidity	HA	5		95	%	

Recommended Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Case Operating Temperature	Tcase	0		70	° C	
Ambient Humidity	HA	5		70	%	Non-condensing
Power Supply Voltage	VCC	3.13	3.3	3.47	V	
Bit Error Ratio	BER				10-12	
Max Supported link Length						
Coupled Fiber	Single mode fiber					9/125um SMF

Electircal Characteristics (Tc =0°C to 70 °C and Vcc= 3.14 to 3.46)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note		
Supply Voltage	Vcc	3.14	3.3	3.46	V			
Supply Current	Icc			300	mA			
Transmitter								
Input Differential Impedance	Rin	80	100	120	Ω			
Differential Data Input Swing	Vin	180		700	mVp-p			
Transmit Disable Voltage	Vdis	2			V			
Transmit Enable Voltage	Ven	Vee		Vee+0.8	V			
Transmit Fault Assert Voltage	Vfa	2.2			V			
Transmit Fault De-Assert Voltage	Vfda	Vee		Vee+0.4	V			
	Rece	iver						
Differential Data Output Swing	Vod	450	600	850	mVp-p			
Output Rise Time	Trise		25		ps			
Output Fall Time	Tfall		25		ps			
LOS Fault	Vlosft	2		Vcc	V			
LOS Normal	Vlosnr	Vee		Vee+0.8	V			



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Optical Characteristics (Tc =0 $^{\circ}$ C to 70 $^{\circ}$ C and Vcc= 3.14 to 3.46)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
	Transı	mitter				
Nominal Wavelength	λ	1530	1550	1560	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Optical Output Power	Pav	0		+4	dBm	
Extinction Ratio	ER	8.2			dB	
Transmitter and Dispersion Penalty		TDP		3.2	dB	
Average Launch Power of OFF Transmitter	Poff			-35	dBm	
Relative Intensity Noise	Rin			-128	dB/HZ	
Optical Return Loss Tolerance	ORLT			12	dB	
	Rece	eiver				
Center Wavelength	λ	1260		1610	nm	
Average Receiver Power	Pavg	-23			dBm	1
Overload input power	Rover			-8	dBm	
Receive Electrical 3db Upper Cutoff Frequency	Fcut			12.3	GHZ	
Los Assert LOS	LOSd	-35			dBm	
Los De-Assert LOS	LOSa			-24	dBm	
Los Hysteresis		0.5			dB	

Note1: Sensitivity for 10G PRBS 2 23-1 and BER better than or equal to 10E-12



Pin function definitions

Table 1: Transceiver pin descriptions

Pin Number	Symbo 1	Name	Description
1,17,20	VeeT	Transmitter Signal Ground	These pins should be connected to signal ground On the host board.
2	TX Fault	Transmitter Fault Out (OC)	Logic "1" Output=Laser Fault(Laser off before t_fault) Logic "0" Output=Normal Operation This pin is open collector compatible, and should be pulled up to Host Vcc with a $10k \Omega$ resistor
3	TX Disabl e	Transmitter Disable In (LVTTL)	Logic "1" Input(or no connection)=laser off Logic "0" Input = Laser on This pin is internally pulled up to VccT with a $10k\Omega$ resistor
4	SDA		Serial ID with SFF-8472 Diagnostics
5	SCL	Module Definition	Module Definition pins should be pulled up to Host Vcc
6	MOD-AB S	Identifiers	with 10 k Ω resistors.
7	RS0	ReceiverRateSelect(LVTTL)	These pins have an internal 30 k Ω pull-down to ground. A
9	RS1	Transmitter Rate Select(LVTTL)	Signal on either of these pins will not affect module performance.
8	LOS	Loss of signal Out(OC)	Sufficient optical signal for potential BER<1x10-12=Logic "0" Insufficient optical signal for potential BER<1x10-12=Logic "1" This pin is open collector compatible ,and should be pulled up to Host Vcc with a 10 kΩ resistor
10,11,14	VeeR	Receiver Signal Ground	This pins should be connected to signal ground on the host board.
12	RD-	Receiver Negative Data Out(CML)	Light on = Logic "0" output receiver data output is internally AC coupled and series terminated with a 50 Ω resistor.
13	RD+	Receiver Positive Data Out(CML)	Light on = Logic "1" output receiver data output is internally AC coupled and series terminated with a 50 Ω resistor.
15	VccR	Receiver Power Supply	This pin should be connected to a filtered +3.3V power supply on the host board .See Figure3.Recommended power supply filter
16	VccT	Transmitter Power Supply	This pin should be connected to a filtered +3.3V power supply on the host board .See Figure3.Recommended power supply filter



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18	TD+	Transmitter Positive data in (CML)	Logic "1" Input =light on Transmitter data inputs are internally AC coupled and terminated with a differential 100Ω resistor
19	TD-	Transmitter Negative data in(CML)	Logic "O" Input =light on Transmitter data inputs are internally AC coupled and terminated with a differential 100Ω resistor

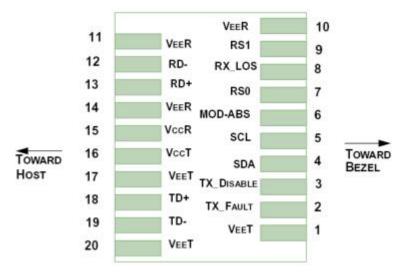


Figure 2. Pin function definitions

Typical application circuit

Recommended "Typical Application Schematics" are shown in Figure 3

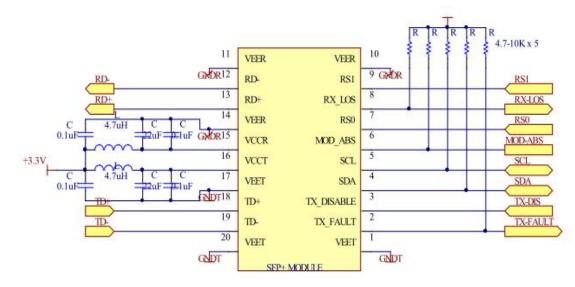


Figure 3. Typical application schematics

Electrostatic Discharge (ESD)



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The WDP863-59C1 is compatible with ESD levels found in typical manufacturing and operating environments as described in Table 2. In the normal handling and operation of optical transceivers, ESD is of concern in two circumstances.

The first case is during handling of the transceiver prior to insertion into an SFP+ compliant cage. To protect the device, it's important to use normal ESD handling pre-cautions. These include use of grounded wrist straps, work-benches and floor wherever a transceiver is handled

The second case to consider is static discharges to the exterior of the host equipment chassis after installation. If the optical interface is exposed to the exterior of host equipment cabinet, the transceiver may be subject to system level ESD requirements

Electromagnetic Interference (EMI)

Equipment incorporating gigabit transceivers is typically subject to regulation by the FCC in the United States, CENELEC EN55022 (CISPR 22) in Europe and VCCI in Japan. The WDP863-59C1 compliance to these standards is detailed in Table 2. The metal housing and shielded design of the WDP863-59C1 minimizes the EMI challenge facing the equipment designer.

EMI Immunity (Susceptibility)

Due to its shielded design ,the EMI immunity of the WDP863-59C1 exceeds typical industry standards.

Feature	Test Method	Performance				
Electrostatic Discharge(ESD)	MIL-STD-883C	Class 1 ($>$ 1500 Volts)				
to the Electrical Pins	Method3015.7					
Electrostatic Discharge (ESD)	Variation of IEC 610	00-4-2 Typically ,no damage occurs with 15KV when the				
tp the Duplex LC Receptacle		duplex LC connector receptacle is contacted by a				
		Human Body Model probe				
Electrostatic Interference	CISPR22 ITE Class B					
	EN55022 Class B	Compliant with standards				
(EMI)	FCC Class B					
	IEC61000-4-3 Calss	Typically show no measurable effect from a 3V/m field				
Immunity	2 EN55024	swept from 80 to 1000MHZ applied to the transceiver				
	Z EN00024	without a chassis enclosure				
		Less than 1000ppm of cadmium ,lead,mercury,hexavalent				
ROHS Compliance		chromium, polybrominated biphenyls, and ploybrominated				
		biphenyl ethers.				

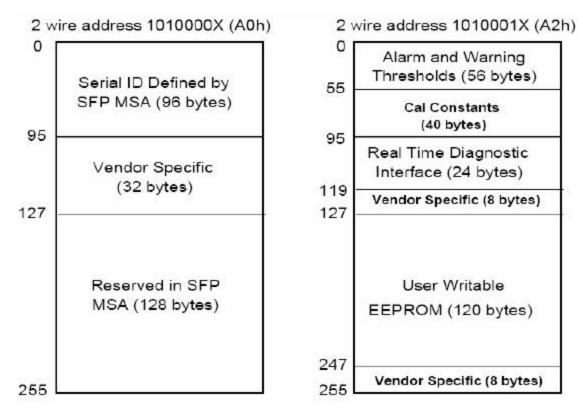
Table 2: Regulatory compliance



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Digital Diagnostic Interface Definition

The 2-wire serial interface addresses of the sfp+ module are 1010000x (AOh) and 1010001x (A2h)



Accessing Serial ID memory uses the 2 wire address 1010000x(AO). Memory Contents of serial ID are shown in Table 3.

Table 3 : Serial ID memory Contents

Data Address	Size (Bytes)	Name of Field	Contents (Hex)	Description					
	BASE ID FIELDS								
0	1	Identifier	03	SFP					
1	1	EXT.Identifier	04	SFP function is defined by					
				Serial ID only					
2	1	Connector	07	LC Connector					
310	8	Transceiver		Transceiver Code					
11	1	Encoding	03	NRZ					
12	1	BR,Norminal	64	9.95~11.3Gbit/s					
13	1	Reserved	00						
14	1	Length(9um,km)	50						
15	1	Length(9um)	FF	Transceiver transmit					
16	1	Length(50um)	00	Distance (80km)					
17	1	Length(62.5um)	00						



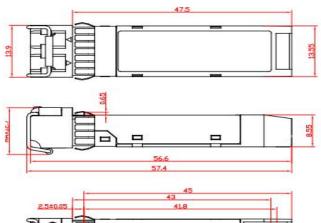
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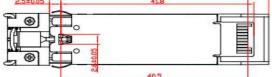
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18	1	Length(Copper)	00				
19	1	Reserved	00				
2035	16	Vendor name	49	4e	46	4f	
			2d	45	54	45	
			52	4e	41	4c	Net-Light(ASCII)
			20	20	20	20	
36	1	Reserved	00				
37—39	3	Vendor OUI	00	00	00		
40—55	16	Vendor PN					WDP863-59C1 (ASC11)
56—59	4	Vendor REV	00	00	00	00	
60—61	2	Wavelength					Transceiver wavelength
62	1	Reserved	00				
63	1	CC_BASE	Che	eck S	Sum		Check code for Base ID Fields
		EXTEDND	ED I	D FI	ELDS		
64—65	2	Options	00	1A			TX DISABLE
							TX FAULT and Losss of
							Signal implemented
66	1	BR, max	00				
67	1	BR,min	00				
68—83	16	Vendor SN	31	32	33	34	Serial Number of transceiver
			35	36	37	38	(ASCII).
			20	20	20	20	For example :12345678
			20	20	20	20	-
84—91	8	Data code	30	35	31	30	Data code :051011
			31	31	20	20	
92	1	Diagnostic	68				Diagnostics (INT.Cal)
		Monitoring Type					
93	1	Enhanced Options	F6				Optional Alarm/Warning flags
							Implemented for all monitored
							quantities, optional soft
							tx_fault monitoring
							implemented ,optional soft
							rx_los monitoring implemented
94	1	SFF-8472	03				Diagnostics (SFF-8472
		Compliance					Rev10.2)
95	1	CC_EXT	Che	eck S	Sum		Check sum for Extended ID
							Field
	·	VENDOR SP	ECIF	IC	ID	FIEL	DS
96127	32	Vendor Specific	Rea	ıd or	nly		Depends on customer information
128- 255	128	Reserved	Rea	ıd or	nly		Filled by zero

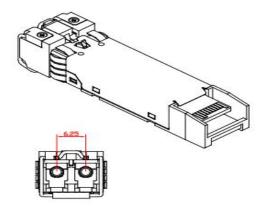


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Package outline (unit:mm)







Unit: mm Unspecified Tolerance: ±0.1mm